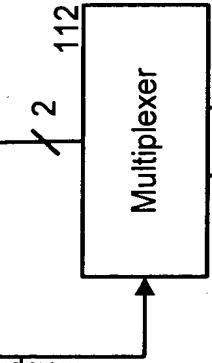
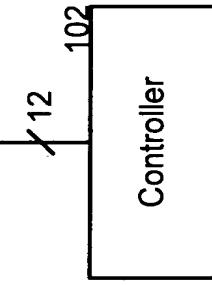
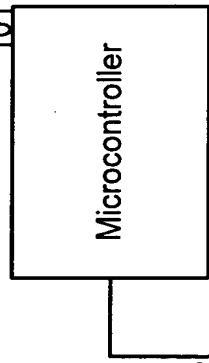


Fig. 1A
(Prior Art)



Multiplexer Control Signals

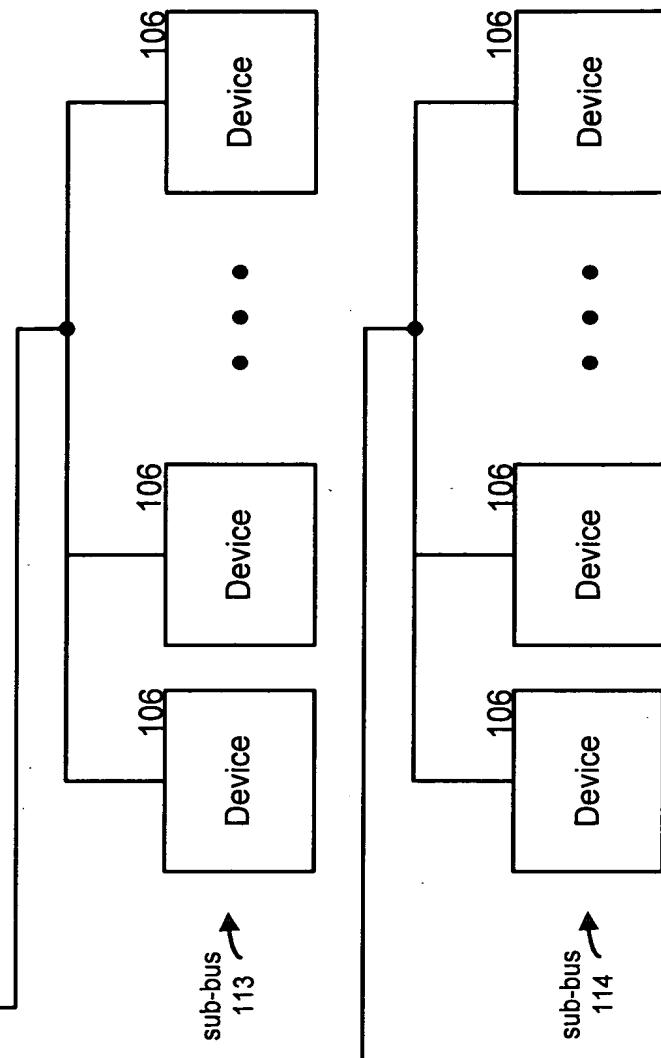
12

102

2

112

113



sub-bus
114

Fig. 1B
(Prior Art)

FIG. 2

200 →

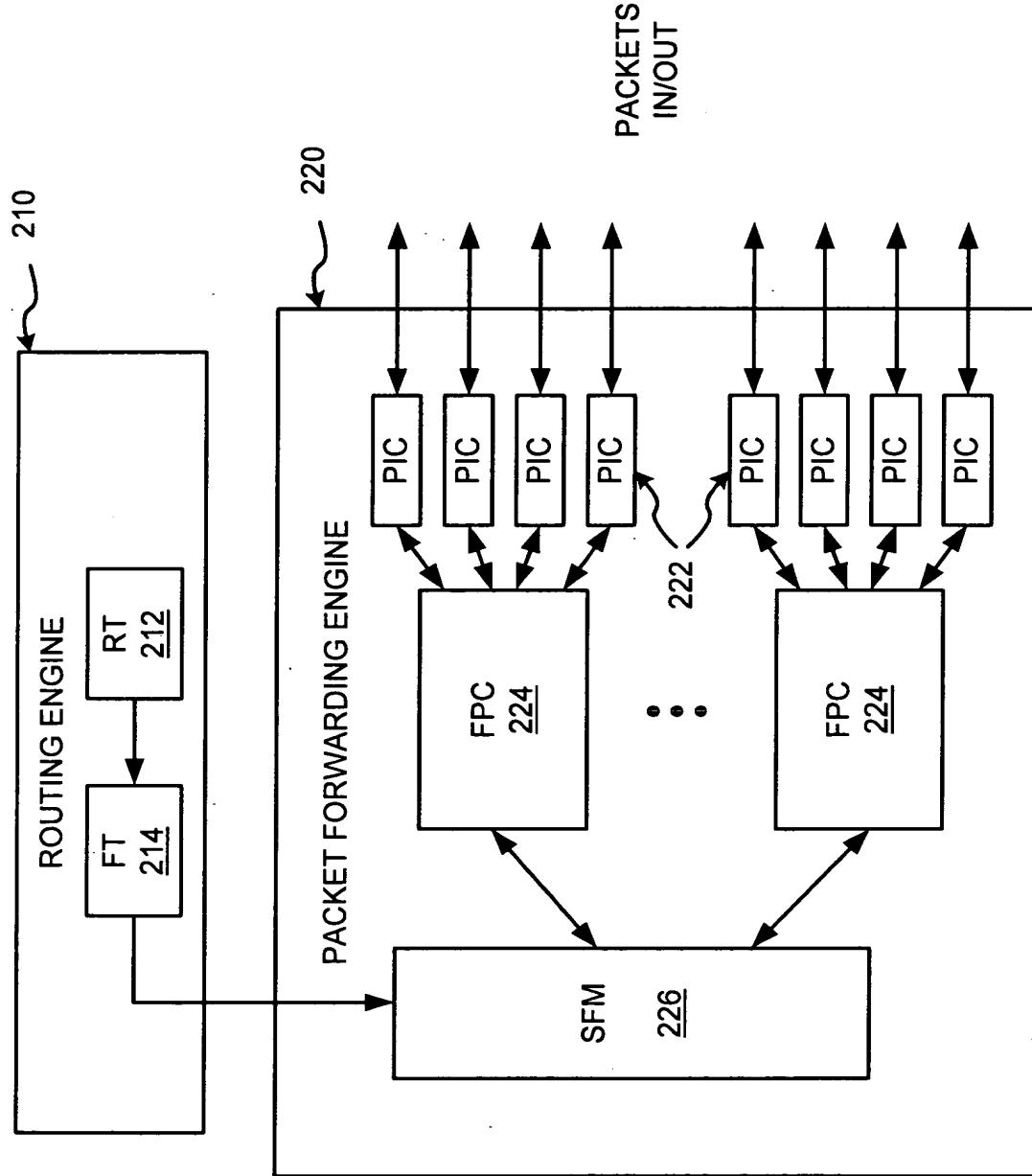


FIG. 3

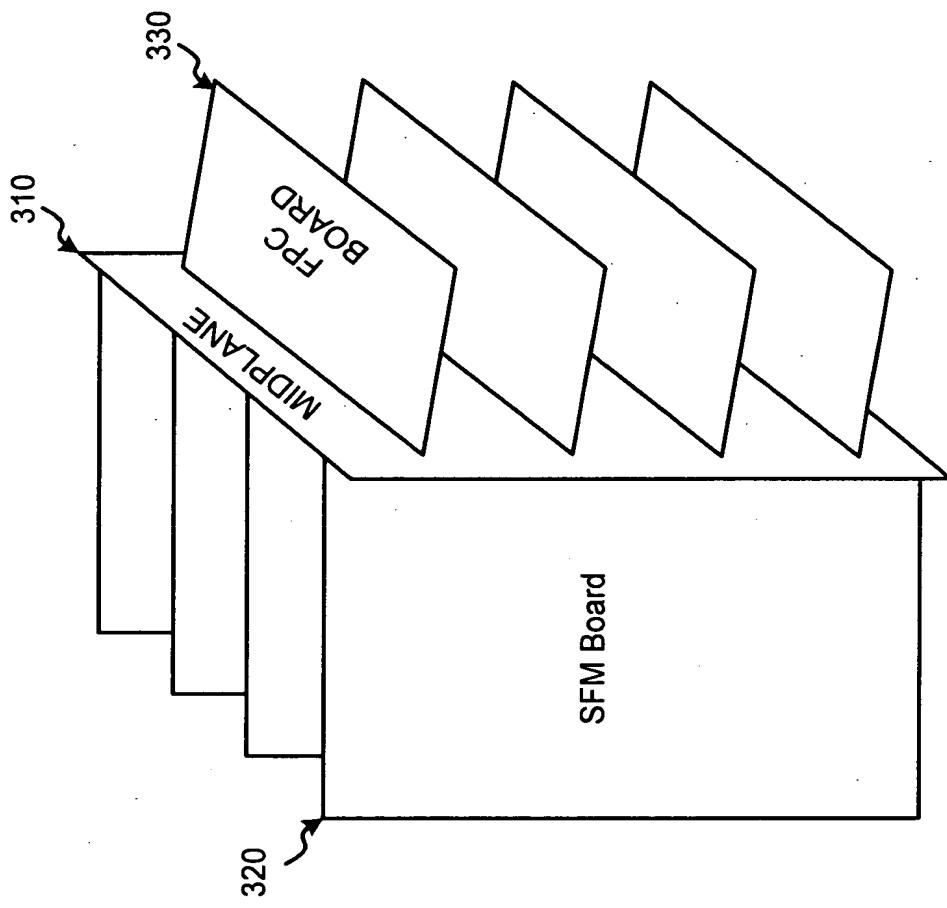


Fig. 4

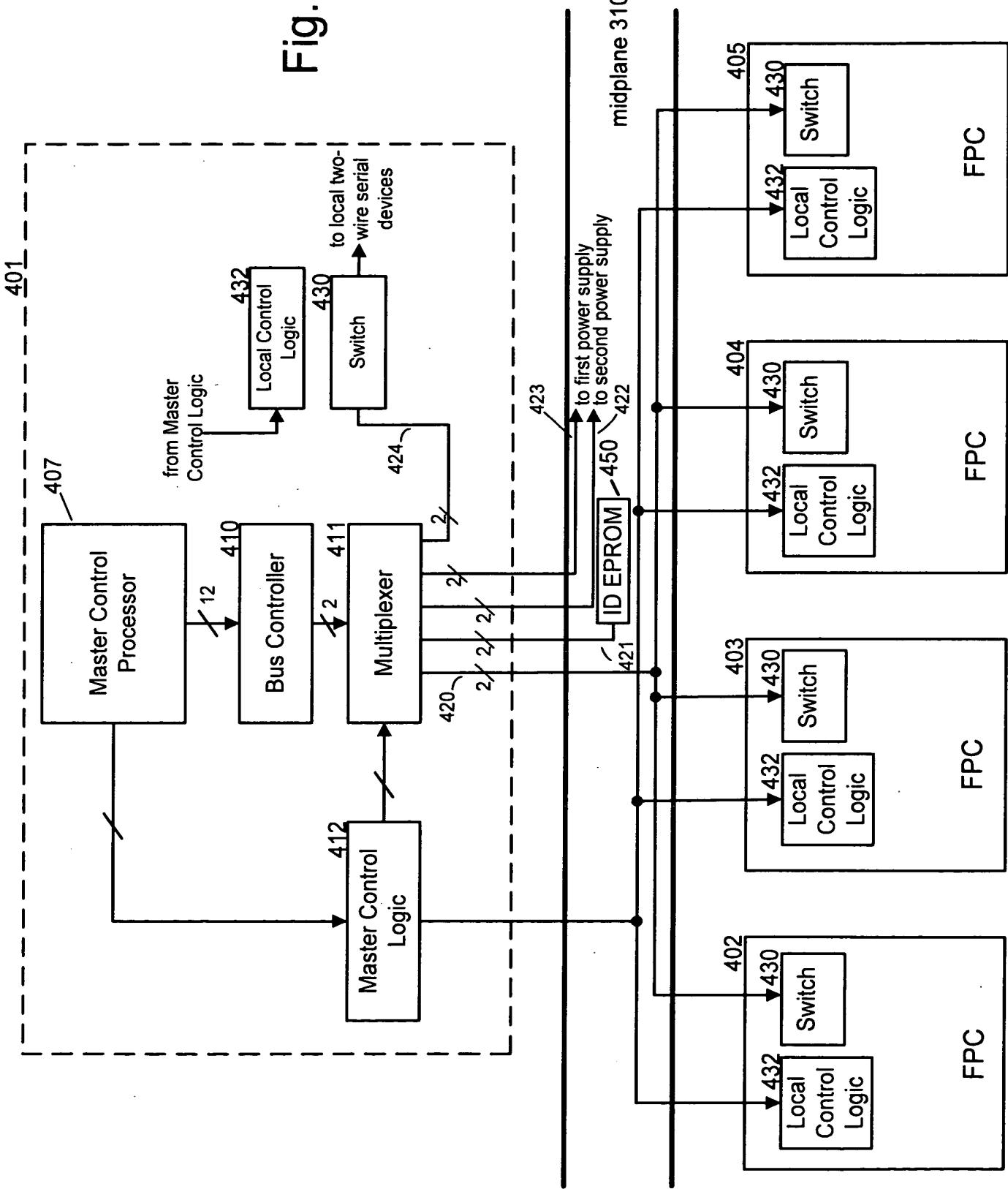
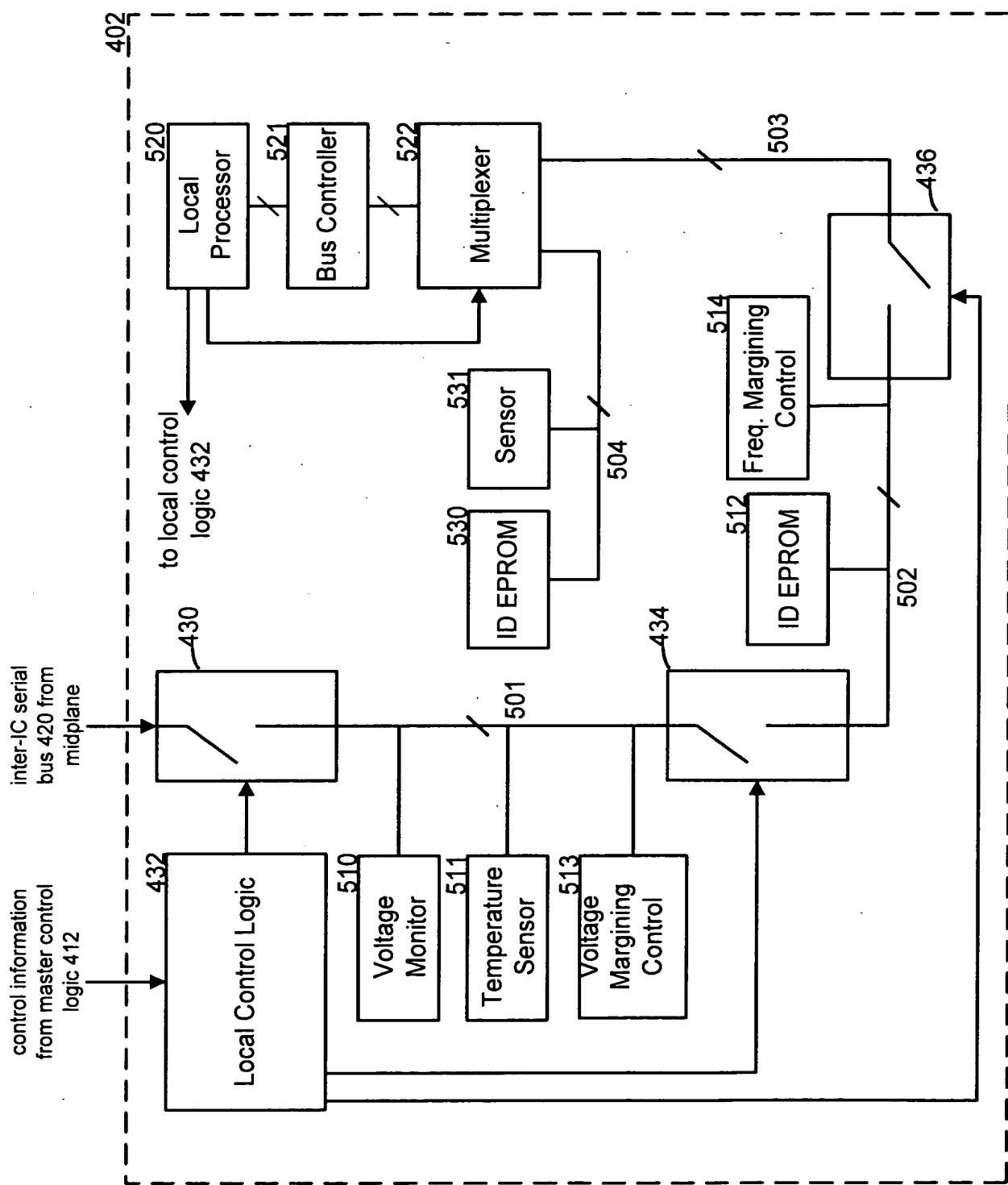


Fig. 5.



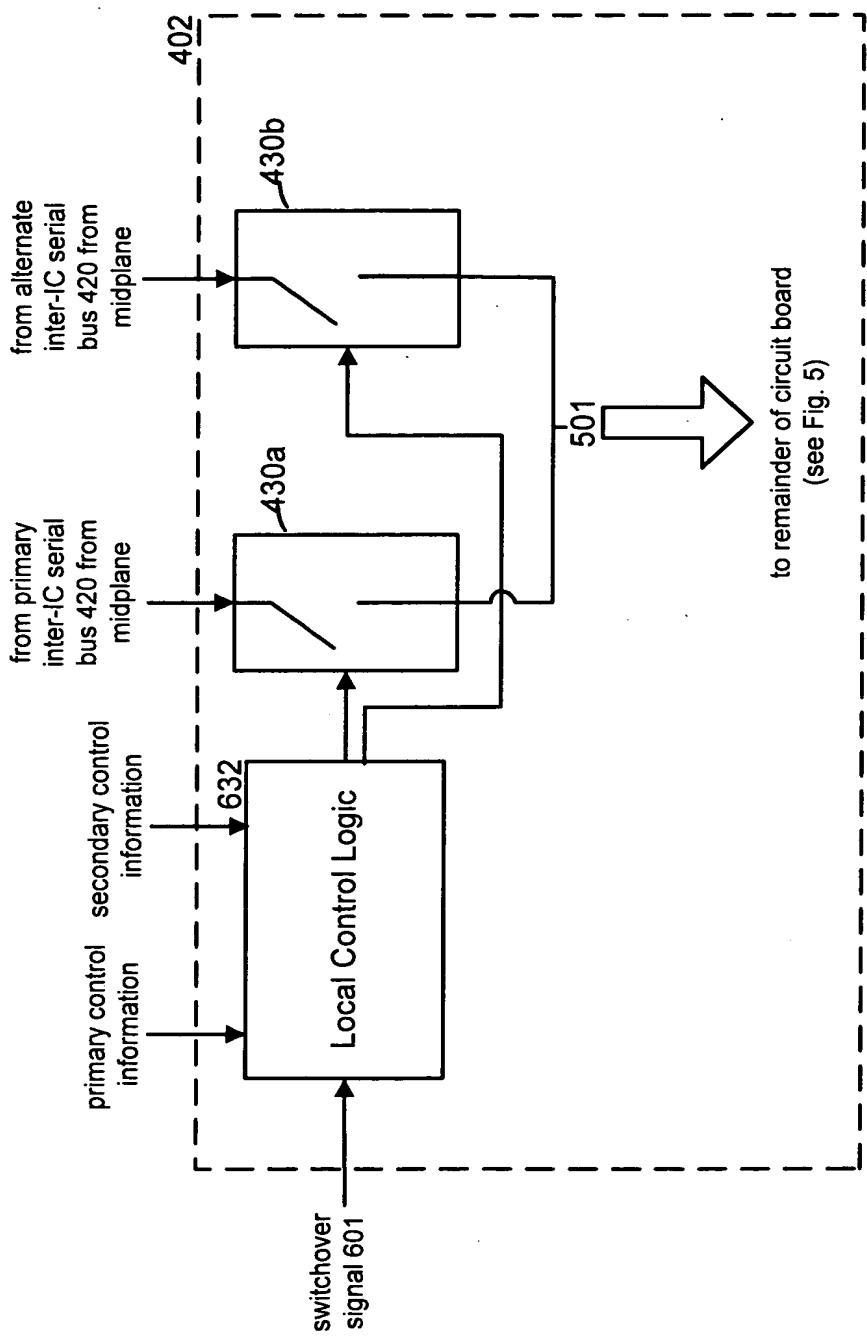


Fig. 6